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6. A circuit involved in either one of claims 4 and 5, in which the part of the circuit having the gaps is formed by a single layer of magnetic material.

5 7. A circuit involved in either one of claims 4 and 5, in which the part of the circuit having the gaps is formed by a stack of alternately magnetic (44) and insulating (46) layers.

¹⁰
Add 17

Add B2

Add
73

Add
C1